

Movidia launches MA1110, reveals little about architecture

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Tuesday, 20 January 2009

We wrote about Movidia in October of last year, when they posted a little introductory video about themselves on their website. Since the company target the mobile space and multimedia editing on the go, we speculated that we should either expect a low-power multi-core architecture, or a Single Instruction Multiple Data (SIMD) vector processor. Even though the company has officially launched the MA1110 multimedia processor, given this press release, we are still not much wiser as to what the MA1110 actually looks like. Other than touting features that the MA1110 supports, such as real-time image stabilization, super resolution zoom, slow motion, and color matching, the press release reveals absolutely nothing about the chip architecture. Richard Wilson, in his column over at ElectronicsWeekly.com, was able to obtain a few additional details: the chip is produced in 65nm, contains a 32-bit RISC core, and has a novel bus architecture that allows for a high level of data throughput. This however will have to do for now, at least until Movidia demonstrates the MA1110 at Mobile World Congress in Barcelona in mid February. Consumer samples of the chip are expected to become available later this Summer.